PCN Number: 20200203005.1 PCN Date: Feb 4, 2020)20				
Title: Qualification of AIZU as an additional Fab Site option for select CMOS7 devices											
Customer C	PCN Manager			Dept:			0	Quality Services			
-					Estimated Sample			0	Date provided at		
Proposed 1 st Ship Date:		May 4, 2020			Availability:			s	sample request.		
Change Typ	e:										
Assemb	ly Site		Assemb	ly Pro	cess			Ass	embly Materia	s	
Design					cification				hanical Specif	ication	
Test Site		Packing/Shipp							Test Process		
Wafer Bump Site		Wafer Bump							Wafer Bump Process		
🛛 Wafer Fab Site			Wafer Fab Ma			terials			Wafer Fab Process		
			Part number change								
PCN Details											
Description of Change:											
Texas Instruments is pleased to announce the qualification of its AIZU fabrication facility as an additional Wafer Fab source for the selected devices listed in "Product Affected" section.											
	Current Sit	es Add				itional Sites					
Current	Process		Wafe				Proc	ess	Wafer		
Fab Site	014007		Diame		Fab Site				Diameter	•	
MAINEFAE	B CMOS7		200m	m	AIZU		СМС	057	200mm		
Qual details are provided in the Qual Data Section. Reason for Change: Continuity of Supply											
Continuity of Supply Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):											
-	Impact on For	m, FI	t, Funct	ion, C	Quality of Re	elladi	lity	(posi	tive / negativ	/e):	
None											
Changes to	product identi	ricati	on resu	Iting	from this P	CN:					
Current											
Chip Site	Chip Site	Origin	n (20L)	Chip	Site Country	Code	(21	L) (Chip Site City		
MAINEFAB	CUA	CUA		USA				-	South Portland		
	MAINERAD			Cont							
New Fab S	lite										
Chip Site	Chip Site	Origin	ı (20L)	Chip	Site Country	[,] Code	(21	L) (Chip Site City		
AIZU	CU2			JPN				A	lizuwakamatsu	ı-shi	
Made IN: M 2DC: MSL 2 /260 MSL 1 /235 OPT: ITEM: LBL: 5A Product Aff	ATS Alaysia 20: C/1 YEAR SEAL D 03/29/ (L)T0:175(ected Group:	2 4 ⊤ 4	t actual	produ	(1P) (Q)	2000 LOT: KY(1	39 T)	(D) 59047 7523	0336 7MLA 4483512 0033517 CCO:USA		
LM3668SD-4550/NOPB											

Qualification Report

Approve Date 24-Jan-2020

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: LM3668QDNTRQ1	
PC	Preconditioning Level 1	Level 1 260C	3/800/0	
ELFR	Early Life Failure Rate, 125C	48 Hours	3/2400/0	
HTOL	Life Test, 125C	1000 Hours	3/231/0	
HAST	Biased HAST, 130C/85%RH	96 Hours	3/231/0	
UHAST	Unbiased HAST 130C/85%RH	96 Hours	3/231/0	
TC	Temperature Cycle, -65/150C	500 Cycles	3/231/0	
HTSL	High Temp Storage Bake 150C	1000 Hours	1/77/0	
CDM	ESD - CDM	1250 V	3/9/0	
HBM	ESD - HBM	2500 V	3/9/0	
LU	Latch-up	Per JESD78	3/18/0	
SD	Surface Mount Solderability	Pb and Free	1/30/0	
WBP	Bond Pull	Wires	3/90/0	
WBS	Ball Bond Shear	Wires	3/90/0	
ED	Electrical Characterization	Per Datasheet Parameters 3/90/0		

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

- The following are equivalent HTOL options based on an activation energy of 0.7 eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours - The following are equivalent HTSL options based on an activation energy of 0.7 eV: 150C/1k Hours, and 170C/420 Hours

- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

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